

Workshop Schedule

Monday

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| 8:00 | <i>Coffee</i> | |
| 8:30 | <i>Session 1</i> | |
| | Personal Introductions | 25 min |
| | Building Plan, etc. | 10 min |
| | Workshop Introduction | 25 min |
| | Why Design Automation | 30 min |
| 10:00 | <i>Coffee Break</i> | |
| 10:30 | <i>Session 2</i> | |
| | How to put together a DA Course | 90 min |
| 12:00 | <i>Lunch</i> | |
| 1:00 | <i>Session 3</i> | |
| | Introduction to Design Automation | 60 min |
| | FHDL | 30 min |
| 2:30 | <i>Coffee Break</i> | |
| 3:00 | <i>Session 4</i> | |
| | FHDL Practice | 90 min |
| 4:30 | <i>End of Day</i> | |

Tuesday

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| 8:00 | <i>Coffee</i> | |
| 8:30 | <i>Session 5</i> | |
| | Intro to Visual Basic/ MS Windows Programming | |
| 10:00 | <i>Coffee Break</i> | |
| 10:30 | <i>Session 6</i> | |
| | Practice with Visual Basic/ MS Windows | |
| 12:00 | <i>Lunch</i> | |
| 1:00 | <i>Session 7</i> | |
| | Logic Design and Computer Architecture with Simulation | |
| 2:30 | <i>Coffee Break</i> | |
| 3:00 | <i>Session 8</i> | |
| | Laboratory Exercises | |
| 4:30 | <i>End of Day</i> | |



But first a few preliminary notes ...

Wednesday

- 8:00** *Coffee*
- 8:30** *Session 9*
How to Design an FPGA Course
- 10:00** *Coffee Break*
- 10:30** *Session 10*
Digital Design Using FPGAs
- 12:00** *Lunch*
- 1:00** *Session 11*
XILINX Tools & Practice I
- 2:30** *Coffee Break*
- 3:00** *Session 12*
XILINX Tools & Practice II
- 4:30** *End of Day*

Thursday

- 8:00** *Coffee*
- 8:30** *Session 13*
XILINX Tools & Practice III
- 10:00** *Coffee Break*
- 10:30** *Session 14*
How to put together a CMOS VLSI Course
- 12:00** *Lunch*
- 1:00** *Session 15*
Introduction to Design Tools
- 2:30** *Coffee Break*
- 3:00** *Session 16*
Lab with Tools
- 4:30** *End of Workshop*



Ahhh, Lunch!