

# Workshop Schedule

## *Monday Aug 4*

<b>8:00</b>	<b>Coffee</b>	
<b>8:30</b>	<b>Session 1</b>	
	Personal Introductions	25 min
	Building Plan, etc.	10 min
	Workshop Introduction	25 min
	Why Design Automation?	30 min
<b>10:00</b>	<b>Coffee Break</b>	
<b>10:30</b>	<b>Session 2</b>	
	FHDL	30 min
	Introduction to Design Automation	60 min
<b>12:00</b>	<b>Lunch</b>	
<b>1:00</b>	<b>Session 3</b>	
	How to put together a Design Automation Course	90 min
<b>2:30</b>	<b>Coffee Break</b>	
<b>3:00</b>	<b>Session 4</b>	
	FHDL Practice	90 min
<b>4:30</b>	<b>End of Day</b>	

## *Tuesday Aug 5*

<b>8:00</b>	<b>Coffee</b>	
<b>8:30</b>	<b>Session 5</b>	
	Logic Design and Computer Architecture with Simulation Exercises	90 min
<b>10:00</b>	<b>Coffee Break</b>	
<b>10:30</b>	<b>Session 6</b>	
	Intro to Visual Basic And MS Windows Programming	90 min
<b>12:00</b>	<b>Lunch</b>	
<b>1:00</b>	<b>Session 7</b>	
	Practice with Visual Basic and MS Windows Programming	90 min
<b>2:30</b>	<b>Coffee Break</b>	
<b>3:00</b>	<b>Session 8</b>	
	Laboratory Exercises	90 min
<b>4:30</b>	<b>End of Day</b>	

## *Workshop Schedule Page 2*

### *Wednesday Aug 5*

<b>8:00</b>	<b>Coffee</b>	
<b>8:30</b>	<b>Session 9</b>	
	How to Design an FPGA Course	90 min
<b>10:00</b>	<b>Coffee Break</b>	
<b>10:30</b>	<b>Session 10</b>	
	Digital Design Using FPGAs	90 min
<b>12:00</b>	<b>Lunch</b>	
<b>1:00</b>	<b>Session 11</b>	
	XILINX Tools & Practice I	90 min
<b>2:30</b>	<b>Coffee Break</b>	
<b>3:00</b>	<b>Session 12</b>	
	XILINX Tools & Practice II	90 min
<b>4:30</b>	<b>End of Day</b>	

### *Thursday Aug 6*

<b>8:00</b>	<b>Coffee</b>	
<b>8:30</b>	<b>Session 13</b>	
	XILINX Tools & Practice III	90 min
<b>10:00</b>	<b>Coffee Break</b>	
<b>10:30</b>	<b>Session 14</b>	
	How to put together a CMOS VLSI Course	90 min
<b>12:00</b>	<b>Lunch</b>	
<b>1:00</b>	<b>Session 15</b>	
	Introduction to Design Tools	90 min
<b>2:30</b>	<b>Coffee Break</b>	
<b>3:00</b>	<b>Session 16</b>	
	Lab with Tools	90 min
<b>4:30</b>	<b>End of Workshop</b>	